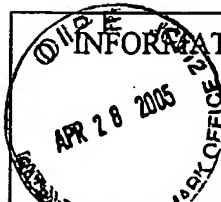


INFORMATION DISCLOSURE STATEMENT				Atty. Docket No. 1765-US		Serial No. Not Assigned 10/675083		
				Applicant: XU, Fang				
				Filing Date: Herewith 09/30/03		Group: Not Assigned 2629		
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							Yes	No
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

	INFORMATION DISCLOSURE STATEMENT	
	Atty. Docket No. 1766-US	Serial No. 10/675,083
	Applicant: Fang Xu	
Filing Date: 9/30/2003		Group: 2829

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							Yes	No
<i>KL</i>	AG	56117495	9/14/1981	Japan	3/64	17/00 <i>KL</i>	<i>Abstract</i>	X
<i>FL</i>	AH	2 538 912 A	7/6/1984	France				X
	AI							

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<i>KL</i>	AJ	International Search Report					
<i>KL</i>	AK	"LOGICALLY CONTROLLED CHIP INTERCONNECTION TECHNIQUE" IBM Technical Disclosure Bulletin, IBM Corp. New York, US - vol. 32, no 3B, 1 August 1989 (1989-08-01), pages 294-299, XP000029849, ISSN: 0018-8689, figure 3					
<i>KL</i>	AL	PATENT ABSTRACT OF JAPAN, JP 56 117495 A (Meisei Electric Co Ltd), 14 September 1981 (1981-09-14) abstract; figures 1,3					
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Examiner <i>Purell</i>	Date Considered <i>05/14/05</i>
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.